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Applicant:	Ian P. Shaeffer et al.	Examiner:	Jonathan J. Johnson
Serial No.:	10/654,177	Group Art Unit:	1725
Filed:	September 3, 2003	Docket No.:	10002500-2
Title:	A METHOD OF FABRICATING A SUBSTANTIALLY ZERO SIGNAL DEGRADATION ELECTRICAL CONNECTION ON A PRINTED CIRCUIT BOARD		

AMENDMENT/REPLY

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This Amendment/Reply accompanies a concurrently filed Request for Continued Examination (RCE) under 37 CFR § 1.114 and is in reply to the Final Office Action mailed February 7, 2006. Please amend the above-identified patent application as follows: